INFORMATION DISCLOSURE CITATION *(Use several sheets if necessary)*	Docket Number (Optional) **BUR9-2000-0071-US1**		Application Number **Not Yet Assigned**	
	Applicant(s) **Pascal A. Nsame**			
	Filing Date **Concurrently Herewith**		Group Art Unit **Not Yet Assigned**	

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
kh		5,438,681	08/01/95	Mensch, Jr.			
ulh		5,455,525	10/03/95	Ho et al.			
ulh		5,761,516	06/02/98	Rostoker et al.			
uh		5,842,031	11/24/98	Barker et al.			
ulh		6,035,364	03/07/00	Lambrecht et al.			
ulh		5,838,684	11/17/98	Wicki et al.			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER DOCUMENTS   *(Including Author, Title, Date, Pertinent Pages, Etc.)*

		Pascal Nsame, Yvon Savaria, VIRTUALISING ON-CHIP BUS INTERFACES FOR IMPROVED EMBEDDED PROCESSOR SYSTEM PERFORMANCE, Institut National Polytechnique de Grenoble, pp. 138-143 (December 14-15, 1999).

EXAMINER Kim T. Huynh	DATE CONSIDERED 4/13/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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